Asim Ali Samejo

Objective

Seeking a technical job which provides an opportunity of applying the knowledge acquired and offering an opportunity to foster in the area of my interest.

Personal Details

<u>Date of Birth:</u> **December 19,1979.**

<u>Nationality:</u> **Pakistani**

<u>Permanent Address:</u> **55, Muslim Housing Society,**

Qasimabad, Hyderabad, Pakistan.

Current Education

Masters in Digital Communications at University of Kiel , Germany

(2002-2004)

Education

Bachelor of Engineering & Technology

In Computer System & Networking (1997 – 2001)
Moscow Power Engineering Institute. (Technical University)

High School Cert. (1995-1997**) 1st Division 77 %**
WAPDA Gov. College Guddu.

Master's Thesis Topic

Two Dimensional Trellis Based Equalization

Any multicarrier system suffers from intersymbol interference ISI in time domain and intercarrier interference ICI in frequency domain. For this reason the problem of equalization for each carrier is essentially 2 dimensional. The optimal receiver in case of perfect channel knowledge is maximum likelihood sequence estimation MLSE. In this thesis we study vector MLSE receiver for joint equalization of all carriers. Due to prohibitive complexity of VMLSE because of its exponential relationship with number of carriers and channel memory length. Numerous suboptimal equalization schemes with channel memory length truncation in time and frequency domain have been studied.

Undergraduate Thesis Project

Design of Data Acquisition System & Computer Interface.

In real-time applications, it is of great importance to monitor and respond to physical signals which are of analog nature. Digital signal processor based data acquisition systems DAS can effectively perform operations such as spectral analysis (FFT) and many other mathematical operations on incoming analog signals and decision can be used for control or passed on to computer for further processing or remote monitoring. The focus of this

work is to design the layout of a data acquisition system satisfying some certain design constrains and performance/complexity tradeoff for different mathematical library functions was carried out on ADSP-2181 chip.

Major projects	Design and implementation of audio coder using psycho-acoustic model.
	Detailed analysis of financial and technical aspects of fiber-optic submarine networks

Computer Skill

Languages: Assembly**,** java**,** C/C++**,** VB, HTML.

Graphical Tools: Macromedia, Swish.

Engineering Software: MatLab, PCAD, Labview.

Utility Software: Ms-Office2000, Latex and beamer.

Operating systems: Linux, windows 9x/2k/xp

Linux & windows connectivity, services and packages

Additional Qualification

Introduction to voice and data integrated communication for corporat organizations. (configuration of digital and analog communication gateways)

Configuration of VoIP telephones of different vendors

2nd Seminar on wireless communications 6-8th October 2004 Universi of Kiel

Expertise on Cisco routing and switching

Languages

English: fluent(written and spoken) TOEFL score 271 points

Russian: Advanced level (written and spoken) studied for 4 years

German: Basic level (grundstufe II)

Urdu: mother tongue

Work Experience	- System administrator department of civil engineering MUET Jamshoro aug. 2001- nov.2001

Installed & maintained civil department CAD lab and intra-network.

- Teaching assistant ISRA university Hyderabad nov. 2001-oct. 2002

Conducted laboratories for undergraduate students in the field of computer networks and digital circuit design.

Memberships	Member of IEEE computer and communication society for 5 year.

Areas of Interest	- Optical modulation schemes, Channel coding for optical trans. systems.

- Convolutional codes, viterbi algorithm, dynamic programming.

- Space time codes and iterative codes.

- Transceiver architectures, software radios

- Low bit-rate speech & audio coders.

- Routing protocols and IP based networks.

- Design of digital circuit

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